

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Khater Examiner:
Serial No.: 10/709,222 Group Art Unit:
Filed: April 22, 2004 Attorney Docket No: FIS920030410SUS1
Title: Structure and Method of Forming A Bipolar Transistor Having A Self-Aligned Raised Extrinsic Base Using A Link-up Region Formed From An Opening Therein
Confirmation: 3221

Request for Certificate of Correction

Commissioner for Patents
Office of Patent Publication
ATTN: Certificate of Correction Branch
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby requests that the attached request for correction to the above named application be entered by the Commissioner. Applicant with no deceptive intent inadvertently failed to identify in the first paragraph of the specification the rights of the US Government in the application.

A fee in accordance with 37 CFR 1.20(a) should be charged to deposit account No. 09-0458. No additional fees are believed to be due in connection with this paper. However, if there is any such fee due, please charge any such fee to the deposit account No. 09-0458.

Respectfully submitted,

/Ian D. MacKinnon/

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Registration No. 34,660

Dated: February 17, 2010

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6972443

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APPLICATION NO.: 10/709,222

ISSUE DATE : December 6, 2005

INVENTOR(S) : Khater, Marwan

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Prior to the background of the invention,
please insert:

Notice of Government rights

"This invention was made with Government support under N66001-02-C-8014 awarded by DARPA. The Government has certain rights in this invention."

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

CONFIRMATORY INSTRUMENT

APPLICATION FOR (TITLE OF INVENTION)

Structure and Method of Forming A Bipolar Transistor Having A Self-Aligned Raised Extrinsic Base Using A Link-up Region Formed From An Opening Therein

INVENTOR(S): Khater, Marwan

SERIAL NO. 10/709222	FILING DATE: 4/22/2004
CONTRACTOR: INTERNATIONAL BUSINESS MACHINES CORPORATION	CONTRACT NO. N66001-02-C-8014

The Invention identified above is a "Subject Invention" under the Patent Rights Clause included in the above contract with:

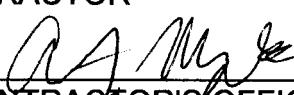
DARPA
(SPECIFY GOV'T. AGENCY HERE)

This document is confirmatory of the paid-up license granted to the Government under the above contract in the Subject Invention, this patent application and any resulting patent(s), and of all other rights acquired by the Government by the referenced clause.

The Government is hereby granted an irrevocable power to inspect and make copies of the above-identified patent application.

Signed this 22 day of December, 2009

INTERNATIONAL BUSINESS
MACHINES CORPORATION
CONTRACTOR

BY: 
CONTRACTOR'S OFFICIAL AND TITLE

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IBM Disclosure No.: FIS820030365

IBM Docket No.: FIS920030410